

		Source						
		B, C, D, E, H, L	Imm	(HL)	(IX+d) (IY+d)	(BC) (DE)	Ext (abs)	
8-bit LD Isetta, Z80	A	2, 4	3, 7	4, 7	8, 19	4, 7	4, 13	
Dest	A	2, 4	3, 7	4, 7	8, 19	4, 7	4, 13	
	B, C, D, E, H, L	2, 4	3, 7	5, 7	9, 19			
	(HL)	4, 7	5, 10					
	(BC),(DE)	4, 7						
	(IX+d),(IY+d)	8, 19	8, 19	8, 19				
	Ext (abs)	4, 13						

		Source				
		B, C, D, E, H, L	Imm	(HL)	(IX+d) (IY+d)	
8-bit Arithmetic, Logic Isetta, Z80	A	5, 4	4, 7	6, 7	10, 19	
	ADD	5, 4	4, 7	6, 7	10, 19	
	ADC	5, 4	4, 7	6, 7	10, 19	
	SUB	5, 4	4, 7	6, 7	10, 19	
	SBC	6, 4	5, 7	7, 7	11, 19	
	AND	3, 4	3, 7	5, 7	9, 19	
	XOR	3, 4	3, 7	5, 7	9, 19	
	OR	3, 4	3, 7	5, 7	9, 19	
	CP	5, 4	4, 7	6, 7	10, 19	
	INC	4, 4		6, 11	10, 23	
	DEC	4, 4		6, 11	10, 23	
	NEG	9, 8				
	CPL	3, 4				

		Source/Dest			
		B, C, D, E, H, L	(HL)	(IX+d) (IY+d)	
Rotate, Shift, Bit Isetta, Z80	A	5, 4			
	RLCA	5, 4			
	RRCA	5, 4			
	RLA	4, 4			
	RRA	4, 4			
	RLC	8, 8	8, 8	8, 15 13, 23	
	RRC	9, 8	10, 8	13, 15 18, 23	
	RL	7, 8	7, 8	7, 15 12, 23	
	RR	8, 8	9, 8	12, 15 17, 23	
	SLA	7, 8	7, 8	7, 15 12, 23	
	SRA	9, 8	10, 8	13, 15 18, 23	
	SRL	7, 8	8, 8	11, 15 16, 23	
	BIT 0-7	7, 8	7, 8	7, 12 12, 20	
	RES 0-7	7, 8	7, 8	7, 15 12, 23	
	SET 0-7	7, 8	7, 8	7, 15 12, 23	

EX AF,AF'	11, 4
EXX	4, 4
EX DE,HL	11, 4
EX (SP),HL	15, 19
EX (SP),IX	17, 23
LDIR	10, 21
CCF	2, 4
SCF	2, 4

		Source							
		Imm	Ext (abs)	HL	BC DE	IX IY	SP	AF	(SP) pop
Dest	EXT (abs)			7/9, 16	9/11, 20	8/10, 20	9/11, 20		
	HL	5, 10	7/9, 16						11/13, 10
	BC DE	5, 10	9/11, 20						11/13, 10
	IX, IY	7, 14	8/10, 20						13/15, 14
	SP	5, 10	9/11, 20	5, 6		6, 10			
	AF								14/16, 10
	(SP) push			10/13, 11	10/13, 11	12/15, 15		15/18, 11	

Long cycle times for: EXT (abs), when the address is not word-aligned  
 PUSH/POP, when SP crosses a page

		Source			
		HL	BC DE	IX IY	SP
	INC	4/5, 6	5, 6	7, 10	5, 6
	DEC	5, 6	5, 6	7, 10	5, 6
	ADD HL	5, 11	7, 11		7, 11
	ADD IX, ADD IY		8, 15	7, 15	8, 15
	ADC HL	12, 15	12, 15		12, 15
	SBC HL	12, 15	12, 15		12, 15

		uncond	condition Z or NZ	condition other
		Program flow	JP	6, 10
JR	7, 12		4/8, 7/12	3/7, 7/12
DJNZ			6/10, 8/13	
JP (HL)	4, 4			
JP (IX), JP (IY)	6, 8			
CALL	18/22, 17		6/19/23, 10/17	5/18/22, 10/17
RET	10/12, 10		4/11/13, 5/11	3/10/12, 5/11
RST	14/18, 11			
NOP	2, 4			

Long cycle times for:  
 RET when SP crosses a page  
 CALL when SP crosses a page  
 Short cycle time when jump not taken

Z80 only, not available for 8080